Silicon Labs Simplifies IEEE 1588 System Integration with New Timing Solution

--Highly integrated modules combine PTP hardware, software and offer ClockBuilder Pro configurability--

AUSTIN, Texas – Nov. 16, 2020 – Silicon Labs (NASDAQ: SLAB), a leading provider of silicon, software and solutions, today announced a new complete solution designed to simplify implementation of IEEE 1588 in communications, smart grid, financial trading and industrial applications. Silicon Labs' ClockBuilder Pro™ software, an industry-leading, highly versatile software tool, enables designers to accelerate development of IEEE 1588 system integration by combining PTP profile selection, PTP network configuration, and physical-layer clock/port configuration in a single, unified software utility.

"Silicon Labs is committed to providing the industry's most simplified solutions to ease the adoption of IEEE 1588," said James Wilson, General Manager of Timing Products at Silicon Labs. "Extending ClockBuilder Pro support to our IEEE 1588 modules helps customers accelerate time to market while eliminating the system design challenges associated with less integrated solutions."

The adoption of IEEE 1588 packet-based time synchronization is proliferating beyond communication networks into an increasingly broad range of emerging applications where system designers may have limited prior experience with timing and synchronization. One key design challenge facing engineers is optimizing IEEE 1588 system-level performance, a function of board-level hardware/software design as well as network impairments, such as the packet delay variation caused by changing traffic loads.

Silicon Labs' ClockBuilder Pro provides a robust, reliable solution by combining PTP profile selection, clock/port programming and simple control of Silicon Labs' AccuTime™ IEEE 1588 software to configure operation for a wide variety of network conditions and topologies. Silicon Labs IEEE 1588 modules are standards-compliant with telecom (G.8265.1, G.8275.1 and G.8275.2) , power (IEEE C37.238-2011 and 2017), broadcast video (SMPTE 2059.2), and default profiles, while meeting the stringent timing and synchronization requirements outlined in ITU-T G.8261, G.8273.2 (T-BC, T-TSC), G.8273.4 (T-BC-P and T-TSC-P), G.8262, G.812, G.813 and Telcordia GR-1244-CORE / GR-253-CORE.

To download ClockBuilder Pro, visit silabs.com/developers/clockbuilder-pro-software. For more information about Silicon Labs IEEE 1588 modules, visit silabs.com/timing/network-synchronizers/ieee-1588-modules.

About Silicon Labs

Silicon Labs (NASDAQ: SLAB) is a leading provider of silicon, software and solutions for a smarter, more connected world. Our award-winning technologies are shaping the future of the Internet of Things, Internet infrastructure, industrial automation, consumer and automotive markets. Our world-class engineering team creates products focused on performance, energy savings, connectivity and simplicity. silabs.com

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